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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Mathias Passlack et al.

Patent No.: 5,945,718

Title: PROCESS FOR FORMING A  
SEMICONDUCTOR DEVICE  
INCLUDING A CAPACITOR

) Docket No.: GE04270

) Issue Date: August 31, 1999

**Certificate**

NOV 10 2004

**of Correction**

Commissioner for Patents  
Alexandria, VA 22313

SUBMISSION OF CERTIFICATE OF CORRECTION

Dear Commissioner:

Patentee in the matter of the above-referenced patent hereby requests, pursuant to 37 C.F.R. §1.323, the issuance of a Certificate of Correction. Enclosed, in duplicate, is a Certificate of Correction for a Patent Office error in the subject patent. Please enter this correction.

Please charge the fee for this Certificate of Correction, (\$100.00, see 37 C.F.R. §1.20(a)) to Freescale Semiconductor, Inc. Deposit Account 503079. One copy of this page is enclosed.

Respectfully submitted,

11/1/04  
Date

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313 on:

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Elaine Cox  
Signature

Elaine Cox  
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(Also From PTO-1050)

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 5,945,718  
DATED : August 31, 1999  
INVENTORS: Matthias Passlack et al.

It is certified that error(s) appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Inventors section delete "Shiyi Yu" and replace with --Zhiyi (Jimmy) Yu--

In Inventors section add --Ravindranath Droopad, Tempe--

In claim 6 column 6, line 6, delete "Sisaid" and replace with --Si, said--.

MAILING ADDRESS OF SENDER:  
Freescale Semiconductor, Inc.  
Law Department  
7700 West Parmer Lane  
MD/TX32/PL02  
Austin, Texas 78729

PATENT NO.: 5,945,718  
No. of additional copies: 1

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